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SIDDHARTH INSTITUTE OF ENGINEERING & TECHNOLOGY:: PUTTUR
(AUTONOMOUS)

B.Tech II Year II Semester Supplementary Examinations Dec 2019

COMPUTER ORGANIZATION
(CSE & CSIT)

Time: 3 hours

Max. Marks: 60

(Answer all Five Units 5 x 12 = 60 Marks)

UNIT-I

- 1 a Explain about the Structure of Bus and types of Bus with neat diagram. 8M
b Write in detail about Data Transfer Instructions. 4M

OR

- 2 a Write in detail about Addressing Modes and its types. 7M
b Write in detail about Program Control Instructions. 5M

UNIT-II

- 3 a Draw the H/W Flowchart and H/W Algorithm for Multiplication for positive numbers with a suitable example. 8M
b Carry - Save Addition of Summands. 4M

OR

- 4 a Bit-pair recoding method of multipliers. 6M
b Illustrate Bit-Pair recoding of multipliers derived from Booth recoding with example? 6M

UNIT-III

- 5 a Explain about Hardwired Control with the help of a neat diagram. 8M
b Write out Register Representations and way it is used. 4M

OR

- 6 a Write about Bus transfer with neat diagram? 6M
b Explain about the way of constructing a 4 line common bus system using multiplexers with a neat diagram?. 6M

UNIT-IV

- 7 a Explain about Magnetic Disks in detail with neat diagram. 6M
b Explain about Magnetic Disks in detail with neat diagram. 6M

OR

- 8 a Explain in detail about Cache Memory with Page Replacement Algorithms. 8M
b Explain about Memory Management Requirements. 4M

UNIT-V

- 9 a What is DMA? Draw the block diagram for DMA controller and explain about DMA transfer in a computer. 7M
b List out few I/O Interfaces and explain about them. 5M

OR

- 10 a Explain about Inter Processor Arbitration with neat sketch? 6M
b Explain about characteristics of Multiprocessor? 6M

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